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PATENT & TRADEMARK OFFICE

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 09/596,863
Filed: June 19, 2000
Inventor(s):
Novak, Istvan

Title: Bypass Capacitor Methods For Achieving A Desired Value Of Electrical Impedance Between Parallel Planar Conductors Of An Electrical Power Distribution Structure And Associated Electrical Power Distribution Structures

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Examiner: Bettendorf, J.
Group/Art Unit: 2817
Atty. Dkt. No: 5181-62800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, DC 20231, on the date indicated below.

B. Noel Kivlin
Printed Name

Signature 12-18-03 Date

SUBMISSION OF FORMAL DRAWINGS

Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

B. Noël Kivlin
Reg. No. 33,929

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